Power converter circuits: A hybrid dynamical case

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Abstract
The hybrid paradigm (often referred to as Cyber-Physical-Systems) can be employed to understand (by modelling) or to manipulate (by control design) the dynamical behavior of systems. In this paper, a system of wide use in applications (*Buck*-type power converter) with a simple type of controller (On-Off) is addressed from a perspective of hybrid modelling, developed upon a set-based formulation scheme. This approach is novel in the sense that it allows the formulation of generic formal rules as sets for the transition between continuous and discrete modes of a hybrid model, which can be further implemented as software routines for simulation purposes. Indeed, it is shown how the controlled system can be understood as the union between the system and controller sets. Numerical results obtained with a commercial circuit simulator were replicated by evaluating the set-based formulations, constituting a valuable tool in the path to understand the behavior of complex discontinuous systems.

Keywords: buck-type power converter; hybrid dynamical system; modeling; on-off controller.

Convertidores de potencia: Un caso dinámico híbrido

Resumen
El paradigma híbrido (o de Sistemas Ciber-físicos) puede ser empleado para entender (modelar) o manipular (controlar) el comportamiento dinámico de sistemas. Este artículo aborda el modelado desde una perspectiva híbrida para describir la dinámica de un circuito convertidor de potencia bajo la acción de un controlador encendido-apagado, a través de una formulación basada en conjuntos. Este enfoque es novedoso en cuanto permite formular reglas genéricas pero formales, a partir de transiciones entre modos de operación del sistema, lo cual facilita su posterior implementación computacional en entornos de simulación. De hecho, se muestra como el sistema controlado puede explicarse en términos de la unión de los conjuntos que describen el circuito y su control. Resultados generados en un simulador comercial de circuitos replican las predicciones obtenidas tras evaluar las reglas de conjuntos propuestas, representando un paso importante en el camino hacia la comprensión del comportamiento dinámico de sistemas discontinuos más complejos.

Palabras clave: controlador encendido-apagado; convertidor de potencia reductor; modelamiento; sistema dinámico híbrido.

1. Introduction

Dynamical systems when thought as operating under realistic conditions are usually constrained to evolve inside delimited zones of the state-space, as a natural consequence of physical limitations appearing by implementations, such as the saturation of actuation components and the boundaries of motion trajectories. Moreover, different operational modes of the system define particular regions in the solution space and therefore, a conditioned representation of the dynamics. Such interaction of continuous behavior and discrete transition-rules between operation modes is defined in terms of the so-called Hybrid Dynamical Systems (HDS).

An HDS can be stated as one with solution trajectories composed of continuous paths linked by discontinuous transitions obeying a discrete rule [1]. Hybrid models can be employed as simplified representations of complex systems [2-4] by taking advantage of the information reduction provided by discrete domains (e.g. the bouncing-ball system). Then, an HDS is the best way to describe the coupling between the continuous nature of physical systems and the discrete fashion of computations; i.e. a Cyber-Physical System (CPS), as topic of current research interest for development of intelligent computational systems interacting with nature [5,6]. On the other

hand, it is necessary to design more efficient engineering systems in terms of energy consumption, as consequence of an increasing power demand under limited resources [2,7,8]. In particular, for the case of electrical systems it is well known the role of a power converter circuit for delivering to the load, in an efficient manner, the energy stored in a power supply (i.e. with a minimum loss) [9,10]. The operation of a power converter circuit is determined by the commutation pattern of the switching devices in the corresponding circuit topology. Pulse-width modulated signals (PWM) are often employed to accomplish this task. In a PWM-generation block the input is a continuous signal corresponding to the duty cycle, and the output is a discrete signal with only two possible values: ON or OFF [11,12]. Such a feature imposes a discrete change over the continuous nature of an electric circuit, allowing to model the power converter as a hybrid system [1,12]. To model a power converter is, in general, a complicated task given the discontinuities induced by switching devices [13,14]. Nevertheless, there are several works in the literature proposing isolated models for the converter and the control laws [15-18]. This paper shows a general definition for the hybrid system in terms of a set of continuous and discrete components, being applied to link the power converter and a simple control law, in an attempt to model and to explain the behavior of the controlled circuit, as an HDS. The contents of the paper are organized as follows: Section 2 introduces the hybrid dynamical systems and a set-based formulation for modelling its dynamics; Section 3 uses the proposed approach to model a power converter circuit; in Section 4 the hybrid modelling is extended to an On-Off controller being, furthermore, linked to the converter circuit; finally, Section 5 presents numerical validation of the models and discussion of results, with general conclusions given in section 6.

2. Hybrid Dynamical Systems HDS

Hybrid Dynamical Systems (HDS) have been a subject of increasing interest from the earlier developments in the Eastern literature till nowadays. In particular, new methodologies and techniques for analysis and control tasks appeared in an attempt to deal with the impossibility to apply the well-developed classical theory of smooth-dynamical systems. Interested readers are referred to [19] for a deeper review on some developments concerning piecewise-smooth dynamical systems (including hybrid systems) and applications.

As a representative example, let us consider the dynamical system described by:

$$\dot{x} = \begin{cases} F_1(x); & x \in S_1 \\ F_2(x); & x \in S_2 \end{cases}, \qquad (1)$$

with F_1 and F_2 representing two different modes of operation across the discontinuity boundary Σ in Fig. 1 (where the solution space S has been partitioned into S_1 and S_2), generating respectively the flow paths Φ_1 and Φ_2.

Then in order to solve (1) for an arbitrary time $t > 0$, we must be able to detect accurately the crossing instants of the system flow through the boundary Σ for an appropriate commutation of the vector fields F. In general, it is not straightforward to find closed-form analytical solutions for these kinds of trajectory, moreover, when the boundary conditions may include discontinuities in the resulting flow.

As a valid resource, numerical approximations of the trajectories are considered by appropriate formulations of automata resembling conditionals and transitions between discrete and continuous states. Synthesis and conditioning of numerical methods are then of remarkable importance in the analysis of hybrid dynamics. See [20] and references therein for a detailed description of event-driven simulators applied to systems with discontinuous flows.

2.1. A set-based formulation for the hybrid automaton

In general, a dynamical system given by:

$$\dot{x} = f(x, u, t); \ \ x(t_0) = x_0, \qquad (2)$$

where x is the state-vector of n state-variables, $f(.)$ is a continuous function (non-necessarily linear) of the states, u is a vector of allowed inputs and x_0 is a set of initial conditions; can be solved as a trajectory in the state space $\Phi(t) = x(t)$.

In an HDS such flow path $\Phi(t)$ can be described in terms of a combination of continuous sets of points, linked to discrete conditions between them; i.e. by considering the ordered pair $(t, x(u, t))$, each point of the state-space for the solution path $\Phi(t)$ belongs exclusively to any of both, a continuous set C defined by:

$$C = \bigcup_{i=0}^{l-1} \left\{ \begin{matrix} (t, x(u, t)) \in [t_i, t_{i+1}) \times \mathbb{R}^n \ | \\ x(t) \text{ solves } \dot{x} = f(x, u, t); \ \ x(t_i) = x_i \end{matrix} \right\}, (3)$$

where $[t_i, t_{i+1})$ represents the ith interval of the domain where $x(t)$ is smooth for l continuous trajectories; or the discrete set D:

$$D = \bigcup_{i=1}^{l-1} \left\{ \begin{matrix} (t_i, x(u, t_i^+)) \in [t_i, t_{i+1}] \times \mathbb{R}^n \ | \\ \lim_{t \to t_i^-} x(u, t) \neq \lim_{t \to t_i^+} x(u, t) \\ \vee \nexists \lim_{t \to t_i} \frac{x(u,t) - x(u,t_i)}{t - t_i} \end{matrix} \right\}, \qquad (4)$$

where t_i^+ and t_i^- stand respectively for the infinitesimals of time just after and before the discrete transition at t_i.

Hence, the total hybrid trajectory can be stated as:

$$\Phi(t) = x(u, t) = C \cup D \ \ \forall t. \qquad (5)$$

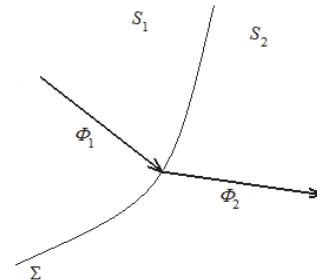Such a result is illustrated in Fig. 2 for $n = 2$. Notice that

Figure 1. Graphical representation of a hybrid dynamical system.
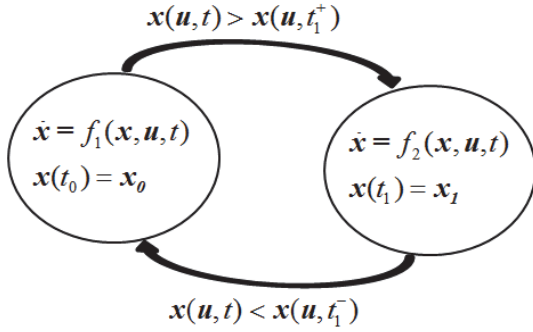Source: Authors

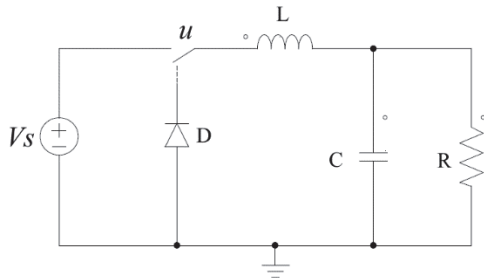Figure 2. Hybrid dynamical system as automaton.
Source: Authors

Figure 4. Conduction mode of the *Buck* circuit.
Source: Authors

Figure 3. *Buck* type DC-DC power converter circuit.
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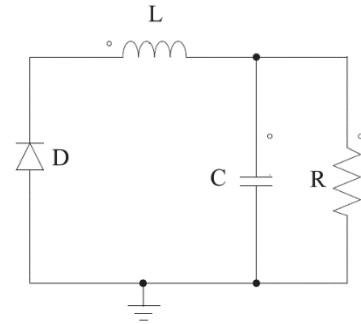
Figure 5. Non-conduction mode of the *Buck* circuit, diode forward-biased.
Source: Authors

it graphically explains, by an automaton, the situation depicted previously in Fig. 1. Then, the system jumps from the descriptions of the vector fields $f_1(x, u, t)$ and $f_2(x, u, t)$ depending on the event occurring at t_1; i.e. $x(u, t) = x(u, t_1)$.

The mathematical representation of HDS introduced here, will be employed in the following to describe the dynamic behavior of a power converter circuit.

3. A *Buck* type power converter circuit

Fig. 3, shows the general circuit describing a buck DC-DC power converter. The operation of the system can be split in two modes depending on the conduction state of the MOSFET transistor which operates as a switch. Let us first consider the operational mode of conduction ($u = 1$).

The equivalent circuit is depicted in Fig. 4. As it can be seen, the source applies the power input to the load. Equations (6) and (7) describe respectively, the Kirchhoff's Voltage (KVL) and Current (KCL) laws applied to the circuit, where V_O represents the output voltage in the load and V_L, V_C the corresponding to inductor and capacitor devices.

$$V_L = V_S - V_O = L\frac{d}{dt}i_L, \tag{6}$$

$$C\frac{d}{dt}V_C = i_L - \frac{V_O}{R}. \tag{7}$$

In the second mode, the switch is open ($u = 0$) and then the energy stored in the capacitor and the inductor should supply the load temporarily until the next conduction mode occurs. Nevertheless, it is necessary to take into account the
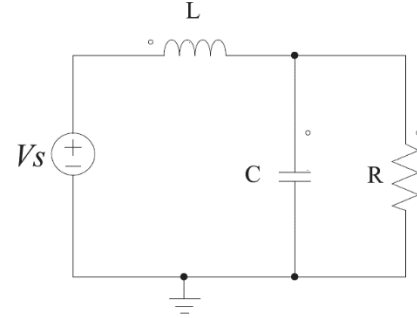
influence of the diode into the circuit. Hence, Fig. 5 shows the equivalent circuit when the diode is forward-biased ($i_L > 0$), with eq. (8)-(9) representing its KVL and KCL equations.

$$V_L = L\frac{d}{dt}i_L = -V_C = -V_O, \tag{8}$$

$$C\frac{d}{dt}V_C = i_L - \frac{V_C}{R}. \tag{9}$$

On the other hand, Fig. 6 shows the non-conduction mode when the diode is reverse-biased. eq. (10)-(11) represent the KVL and KCL equations for the corresponding circuit.

$$V_L = L\frac{d}{dt}i_L = V_C, \tag{10}$$

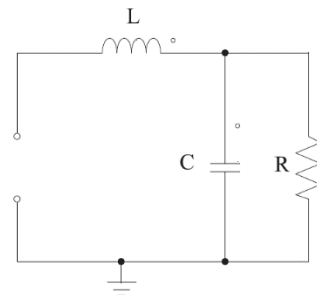$$C\frac{d}{dt}V_C = \frac{V_C}{R}. \tag{11}$$

Figure 6. Non-conduction mode of the Buck circuit, diode reverse-biased.
Source: Authors

Then, after defining as state variables the current of the coil ($x_1(t)$) and the voltage of the load ($x_2(t)$), it is possible to formulate a state-space representation of the circuit according to (2) by combining expressions (6)-(11) in the following matrix representation:

$$\dot{x} = \begin{bmatrix} \dot{x}_1 \\ \dot{x}_2 \end{bmatrix} = \begin{bmatrix} -\dfrac{x_2}{L}\text{sgn}(x_1) + \dfrac{V_S}{L}u \\ \dfrac{x_1}{2C}(1 + \text{sgn}(x_1)) - \dfrac{x_2}{RC}\text{sgn}(x_1) \end{bmatrix};$$

$$x(t_0) = \begin{bmatrix} x_{01} \\ x_{02} \end{bmatrix} \qquad (12)$$

where $x(t_0)$ is the set of initial conditions.

Notice that the *signum* function *sgn(.)* includes the discontinuity to the system flow and, therefore, an HDS representation of the model can be achieved easily, shown as follows.

3.1. A set-based formulation for the Buck circuit

The dynamics of the *Buck*-type power converter circuit depend on the pattern of the PWM signal, given that it provides the time instants where the command input u changes among its binary values.

In a more precise manner, each period T of the PWM signal is composed of an activation interval t_{ON} (for $u = 1$) followed by a gap of inactivity t_{OFF} (for $u = 0$) i.e.

$$T = t_{ON} + t_{OFF}. \qquad (13)$$

Hence, by starting from $t = t_0$, the ith conduction instant can be defined as:

$$t_{i-1} = (i - 1)T + t_0; \quad \forall i \geq 1 \in \mathbb{N}. \qquad (14)$$

Also, the jth inactivity event can be stated by:

$$t_{j-1} = (j - 1)T + t_0 + t_{ON}; \quad \forall j \geq 1 \in \mathbb{N}. \qquad (15)$$

In accordance, the continuous set C describing the solution trajectory can be formulated as:

$$C = \bigcup_{i=0}^{\frac{l-1}{2}} \left\{ \begin{array}{l} (t, x(u,t)) \in [t_i, t_i + t_{ON}) \times \mathbb{R}^n \mid \\ x(u,t) \text{ solves (12)}; u = 1; x(1,t_i) = \begin{bmatrix} x_{i1} \\ x_{i2} \end{bmatrix} \end{array} \right\}$$

$$\bigcup \bigcup_{j=0}^{(l-1)/2} \left\{ \begin{array}{l} (t, x(u,t)) \in [t_j, t_j + t_{OFF}) \times \mathbb{R}^n \mid \\ x(u,t) \text{ solves (12)}; u = 0; x(0,t_j) = \begin{bmatrix} x_{j1} \\ x_{j2} \end{bmatrix} \end{array} \right\}$$

$$, \qquad (16)$$

and equivalently, the discrete events are defined by:

$$D = \bigcup_{i=1}^{\frac{l-1}{2}} \left\{ \begin{array}{l} (t_i, x(1, t_i^+)) \in [t_i, t_i + t_{ON}] \times \mathbb{R}^n \mid \\ \nexists \lim_{t \to t_i} \dfrac{x(u,t) - x(u,t_i)}{t - t_i} \end{array} \right\}$$

$$\bigcup \bigcup_{j=1}^{(l-1)/2} \left\{ \begin{array}{l} (t_j, x(0, t_j^+)) \in [t_j, t_j + t_{OFF}] \times \mathbb{R}^n \mid \\ \nexists \lim_{t \to t_j} \dfrac{x(u,t) - x(u,t_j)}{t - t_j} \end{array} \right\}. \qquad (17)$$

In this way, it is possible to express the system flow, or solution trajectory, for the *Buck*-type converter circuit in eq. (12) by:

$$x(t) = C \cup D \quad \forall t. \qquad (18)$$

4. On-Off controller as a hybrid system

It is well known, that a proportional control action, defined by:

$$u(t) = u(e) = K_P e(t), \qquad (19)$$

where $e(t)$ represents the output error and K_P constitutes the loop gain, is the most basic governing law that can be stated in a feedback control system. Moreover, if $K_P = 1$ we have a unit gain negative feedback created naturally after closing the loop.

If K_P is increased, the disturbance effects start to vanish and steady-state errors become diminished. In practice, however, the maximum value for this loop gain is constrained by saturation boundaries (U_{max} and U_{min}) of signal levels as depicted in Fig. 7.

In spite of this, for higher values of the loop gain K_P, the controller responses are strong enough to annihilate the error, with a limiting theoretical value of $K_P = \infty$. The latter is the special case usually termed On-Off controller, a situation illustrated in Fig. 8 and described mathematically by:

$$u(t) = u(e) = sgn(e(t)). \qquad (20)$$

An On-Off controller delivers as control action, the maximum energy in a minimum time. There are several examples of practical applications for this kind of controller, including heat and cooling systems, level control and end-position control, among others.

As can be seen from (20), the controller has a discontinuous fashion that can be modelled as an HDS.
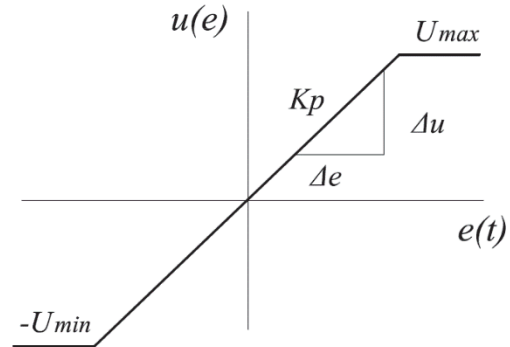

Figure 7. Proportional control action limited by saturation boundaries.
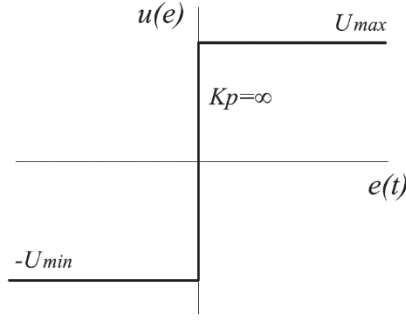Source: Authors

Figure 8. On-Off control action as infinite loop gain of a proportional controller.
Source: Authors

4.1. A set-based formulation for the On-Off controller

In accordance with (2), the dynamical representation of an On-Off controller of the form (20) can be stated as:

$$\dot{u} = g(u, e, t); \quad u(e_0) = u_0, \tag{21}$$

for $\dot{u}$ representing the rate of the control signal $u \in \mathbb{R}$ in terms of variations in $e \in \mathbb{R}$, i.e.

$$\dot{u} = \frac{d}{de} u(e). \tag{22}$$

Then, by considering that a $sgn(.)$ function can be expressed as the following sum of step functions $\mu(.)$:

$$sgn(e(t)) = \mu(e) - \mu(-e), \tag{23}$$

equations (20), (22) and (23) can be combined to give:

$$\dot{u} = \delta(e) - \delta(-e), \tag{24}$$

by using the fact that the derivative of a step function $\mu(.)$ is a Dirac delta function $\delta(.)$.

Then, if dynamics of the On-Off controller (21) can be solved in terms of the ordered pair:

$$(e, u(e)), \tag{25}$$

the continuous set C describing the flow path of the system is:

$$C = \bigcup_{i=0}^{l-1} \begin{Bmatrix} (e, u(e)) \in [e_i, e_{i+1}) \times \mathbb{R}^n \ | \\ u(e) \text{ solves (21)}; u(e_i) = u_i \end{Bmatrix}, \tag{26}$$

and the corresponding discrete events D are given by:

$$D = \bigcup_{i=1}^{l-1} \begin{Bmatrix} \left(e_i, u(e_i^+)\right) \in [e_i, e_{i+1}] \times \mathbb{R}^n \ | \\ \lim_{e \to e_i^-} u(e) \neq \lim_{e \to e_i^+} u(e) \end{Bmatrix}. \tag{27}$$

Therefore, the solution trajectory describing the On-Off control action can be stated as:

$$u(e) = C \cup D \quad \forall e. \tag{28}$$

4.2. Hybrid formulation of the Buck circuit On-Off controlled

The sets proposed in section *2.1* for the continuous paths and discrete events composing the solution trajectory of hybrid systems, further applied in sections *3.1* and *4.1* to describe dynamics of a Buck-type power converter circuit and an On-Off controller, can be merged here to define the hybrid controlled circuit.

In essence, the two models can be coupled by considering that the time instants governing the commutation changes in the circuit, coincide with the zero crossing of the error signal, i.e. by combining equations (16) and (26), the continuous set C describing the flow path of the controlled circuit is:

$$C = \bigcup_{i=0}^{l-1} \begin{Bmatrix} \left(t, \boldsymbol{x}(u(e), t)\right) \in [e_i, e_{i+1}) \times \mathbb{R}^n \ | \\ \mathbf{x}(u(e), t) \text{ solves (12)}; \boldsymbol{x}(u(e), t_i) = \begin{bmatrix} x_{i1} \\ x_{i2} \end{bmatrix}; \\ u(e) \text{ solves (21)}; u(e_i) = u_i \end{Bmatrix}, \tag{29}$$

and equivalently, by combining (17) and (26) the discrete events are defined by:

$$D = \bigcup_{i=1}^{l-1} \begin{Bmatrix} \left(e_i, \mathbf{x}(u(e_i^+), t_i^+)\right) \in [e_i, e_{i+1}] \times \mathbb{R}^n \ | \\ \lim_{e \to e_i^-} u(e) \neq \lim_{e \to e_i^+} u(e) \end{Bmatrix}. \tag{30}$$

Finally, the flow-path of the controlled system can be stated by:

$$\mathbf{x}(u(e), t) = C \cup D \quad \forall e(t). \tag{31}$$

5. Numerical verification of the hybrid models and discussion

The analytic results shown in previous sections have been tested by comparing numerical evaluation of the proposed sets, with results of the commercial circuit simulation package OrCAD-Cadence®.

Figs. 9, 11 show the numerical evaluation in MATLAB® of the data sets given by eq. (18), as solution trajectories of the Buck-type converter circuit, for the parameter values listed in Table 1.

Table 1.
Circuit parameters used in simulations.

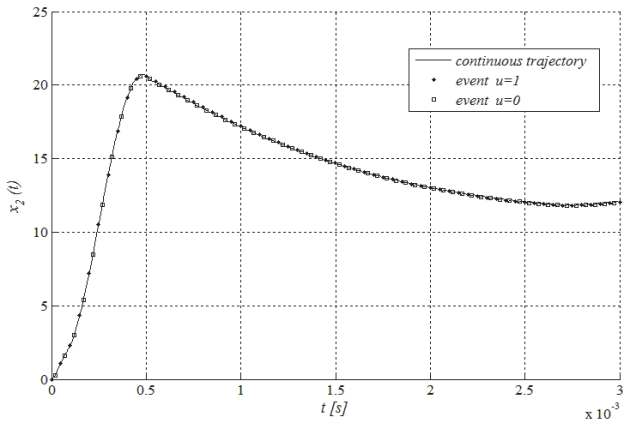| Parameter	Notation	Value
Input voltage	V_S	30 V
Output voltage	V_O	12 V
Switching frequency	f	20 kHz
Duty cycle	D	40 %
Capacitance	C	200 μF
Inductance	L	100 μH
Load impedance	R	5.76 Ω

Source: Authors

Figure 9. Output voltage of a Buck-type power converter reproduced by numerical evaluation in MATLAB® of the hybrid model.
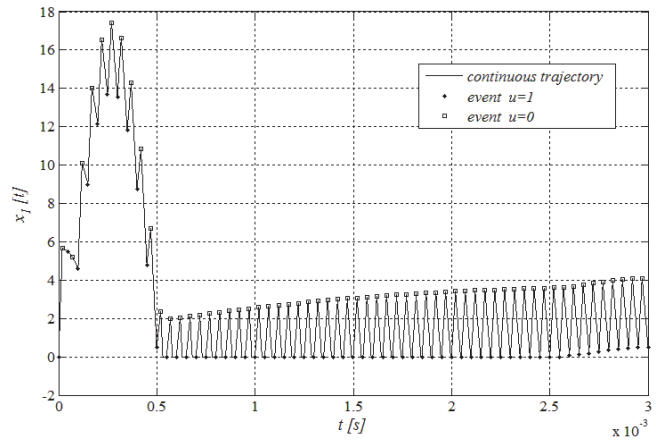Source: Authors

Figure 11. Inductor current of a Buck-type power converter reproduced by numerical evaluation in MATLAB® of the hybrid model.
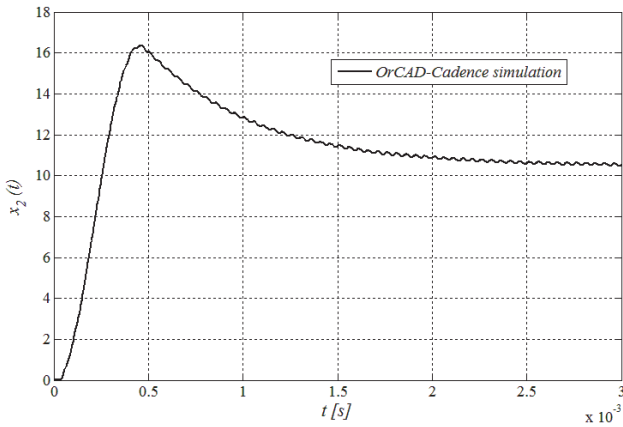Source: Authors

Figure 10. Output voltage of a Buck-type power converter reproduced by numerical simulation in OrCAD-Cadence®.
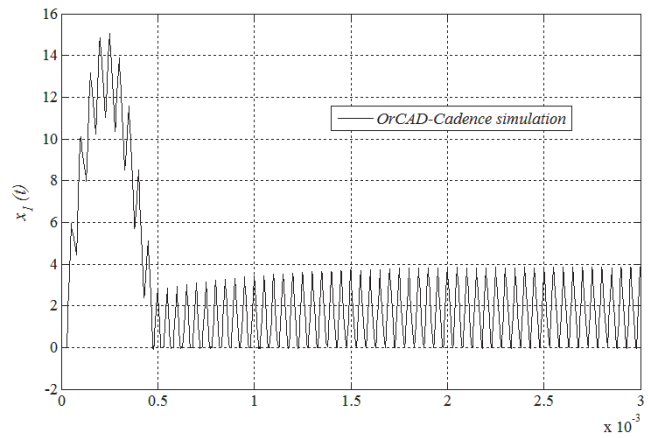Source: Authors

Figure 12. Inductor current of a Buck-type power converter reproduced by numerical simulation in OrCAD-Cadence®.
Source: Authors

In comparison, Figs. 10, 12 reproduce equivalent results by employing OrCAD-Cadence®.

As can be noticed, the qualitative behavior of the voltage and current signals is replicated in both simulation environments. In particular, a smooth shape for the capacitor voltage is achieved with a peak time around 0.5[s] and a falling slope reaching a steady state regime close to the expected value of 12 [V] near to 3 [ms].

In contrast, the behavior of the inductor current is characterized by a discontinuous shape with an initial lobe followed by a stable average of around 2 [A] starting close to 0.5 [s].

However, there are also qualitative disagreements in the results that can be explained by special features added to the commercial circuit simulator (e.g. temperature dependence, non-idealization of circuit elements), not considered in our simple approach. Nevertheless, the agreement achieved is good enough to validate the correctness of the proposed scheme to model the dynamic behavior of the *Buck*-type power converter circuit as an HDS.

Now, in order to verify the hybrid model of the On-Off controller, Fig. 13 shows the synthesis in MATLAB® of a control signal $u(t) = u(e)$, obtained by applying a predefined error waveform $e(t)$ to the system.

The same principle is employed in Fig. 14 to reproduce artificially in MATLAB® the output voltage signal of Fig. 9, by numerical evaluation of the data sets given by eq. (31), as solution trajectories of the Buck-type converter circuit when controlled by the hybrid On-Off action.

In such a case, the error signal applied to the controller as input, has a constant frequency and duty cycle values in accordance to the nominal values illustrated in Table 1. A detail of the waveform generated is illustrated in Fig. 15.

6. Conclusions

An HDS can be represented in terms of a trajectory composed of continuous paths linked by discrete events. Those continuous and discrete intervals have been defined by sets including all the possible points in the state-space which, belonging to the system-flow solutions, can be classified as part of one of the hybrid components.
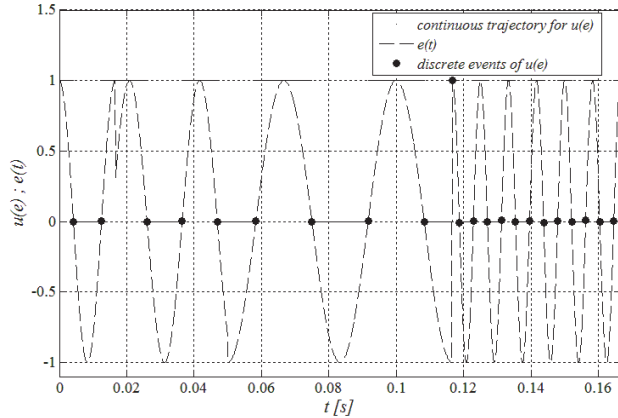
Figure 13. Input-output relationship of On-Off hybrid controller reproduced numerically in MATLAB®.
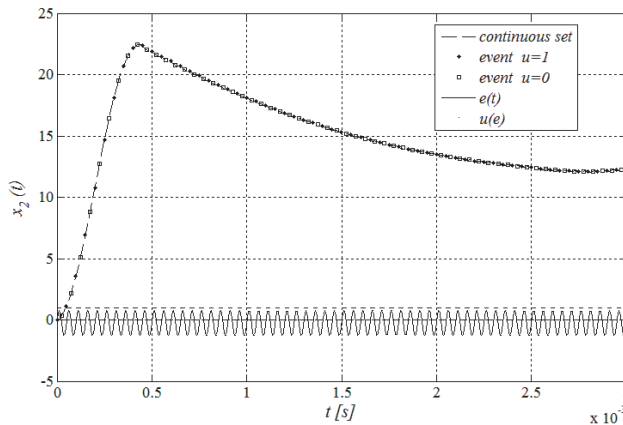Source: Authors

Figure 14. Output voltage of Buck-type converter synthetized by numerical evaluation in MATLAB® of the hybrid model proposed for the controlled circuit.
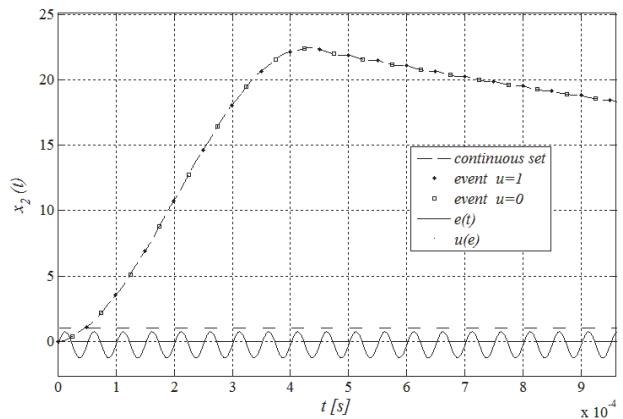Source: Authors

Figure 15. Detail for the output voltage of Buck-type converter synthetized in MATLAB® for the hybrid model proposed.
Source: Authors

A *Buck*-type power converter circuit has been shown as an example of HDS in terms of the proposed set-based

formulation. Numerical evaluation of the hybrid scheme allowed to verify the agreement for capturing the main dynamical features of the system responses compared with a reference framework given by a commercial circuit simulator package.

The proposed set-based approach has been further employed to define an On-Off controller as HDS. In such a case, the dynamics of the control signal at the output of the system was described in terms of variations on the error signal.

An approximation for the On-Off controlled *Buck*-type power converter circuit was then performed by merging the individual representations. As a result, the time instants corresponding to the events where the continuous flow of the circuit solution jumps discontinuously to the next continuous interval, correspond with the zero crossings of the error signal applied as input of the controller.

The numerical results obtained with the commercial circuit simulator were replicated by evaluating the set-based formulation of the controlled circuit, constituting a valuable tool in the path to understand the dynamical behavior of discontinuous systems.

It is interesting to notice the way it was possible to express the behavior of the controlled system, as the union of sets describing individually the circuit and the controller dynamics. This can be understood as a new framework that complements existing approaches, such as finite state machines, Petri nets and Markovian models, by defining formal rules scalable onto more complex systems. The potential of the proposed approach can be exploited by exploring the wide possibilities offered by sets theory in order to describe and to formalize continuous and discrete formulations and interactions in between.

A natural evolution for the application case considered is the study of more complex topologies of power conversion systems, including more than a simple converter with more than a simple controller. This is part of the current ongoing work on which the proposed approach is being applied as a way to model hybrid dynamic behavior.

Acknowledgements

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